

L Number	Hits	Search Text	DB	Time stamp
1	104	(proebsting with robert).IN.	USPAT; US-PGPUB	2003/01/06 11:19
2	0	("5301284 5794059").PN.	USPAT; US-PGPUB	2003/01/06 11:20
3	2	((("5301284") or ("5794059"))).PN.	USPAT; US-PGPUB	2003/01/06 11:20
-	141	link adj repeater	USPAT; US-PGPUB	2002/12/27 11:07
-	107	(Proebsting).IN.	USPAT; US-PGPUB	2002/12/27 11:10
-	57	(Proebsting).IN. and row and column	USPAT; US-PGPUB	2002/12/27 11:11
-	1	("0590243").PN.	USPAT; US-PGPUB	2002/12/27 11:12
-	0	("(590243).AP.").PN.	USPAT; US-PGPUB	2002/12/27 11:12
-	3	("590243").AP.	USPAT; US-PGPUB	2002/12/30 10:55
-	662	(non-volatile or nonvolatile) adj (memory or memories) same (advantage or benefit or utility or usefulness) and @py<=1997	USPAT; US-PGPUB	2002/12/31 13:25
-	1	("4566102").PN.	USPAT; US-PGPUB	2002/12/31 16:23
-	290	(714/10).CCLS.	USPAT; US-PGPUB	2003/01/02 13:23
-	56	(714/10).CCLS. and (integrated adj circuit)	USPAT; US-PGPUB	2003/01/02 10:37
-	9	(714/725).CCLS. and (processor with array)	USPAT; US-PGPUB	2003/01/02 10:57
-	7	(714/732).CCLS. and (processor with array)	USPAT; US-PGPUB	2003/01/02 11:01
-	22	(714/732).CCLS. and (processor with unit\$2)	USPAT; US-PGPUB	2003/01/02 11:02
-	9	(714/732).CCLS. and (processor).TI.	USPAT; US-PGPUB	2003/01/02 11:03
-	12	(714/736).CCLS. and (processor).TI.	USPAT; US-PGPUB	2003/01/02 11:27
-	0	(Karlsruhe).IN.	USPAT; US-PGPUB	2003/01/02 11:28
-	1	(Karlsruhe).IN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/01/02 11:28
-	24	(714/736).CCLS. and processor and (self-check or selfcheck or self-test or selftest)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/01/02 11:57
-	19	(714/736).CCLS. and processor and (self-check or selfcheck or self-test or selftest) and compare	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/01/02 12:05
-	0	(self-check or selfcheck or self-test or selftest) adj processor adj array	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/01/02 12:05
-	51	(self-check or selfcheck or self-test or selftest) adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/01/02 12:06
-	65	(self-checking or self-check or selfcheck or self-test or selftest or self-testing or selftesting) adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/01/02 14:32
-	0	(self-checking or self-check or selfcheck or self-test or selftest or self-testing or selftesting) adj processor adj array	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/01/02 12:06

-	1	(714/10).CCLS. and ("IBM" "IBM BUSINESS MACHINES CORPORATION" "IBM CORPORATION-INTERNATIONAL BUSINESS MACHINES CORPORATION" "IBM CORP.-INTERNATIONAL BUSINESS MACHINES CORP." "IBM INTERNATIONAL BUSINESS MACHINES CORPORATION" "IBM PATENT OPERATIONS").AS.	USPAT; US-PGPUB	2003/01/02 14:36
-	1	(714/5).CCLS. and ("IBM" "IBM BUSINESS MACHINES CORPORATION" "IBM CORPORATION-INTERNATIONAL BUSINESS MACHINES CORPORATION" "IBM CORP.-INTERNATIONAL BUSINESS MACHINES CORP." "IBM INTERNATIONAL BUSINESS MACHINES CORPORATION" "IBM PATENT OPERATIONS").AS.	USPAT; US-PGPUB	2003/01/02 13:27
-	44	(714/10).CCLS. and ("IBM" "BUSINESS MACHINES").AS.	USPAT; US-PGPUB	2003/01/02 13:59
-	49	(712/\$).CCLS. and (self-checking or self-testing or self-check or selfchecking)	USPAT; US-PGPUB	2003/01/02 14:11
-	93	"4191996"	USPAT; US-PGPUB	2003/01/02 14:33
-	1	"3405258".PN.	USPAT	2003/01/02 14:25
-	0	(714/2 ((714/10).CCLS. and ("IBM" "BUSINESS MACHINES").AS.) "3681578".PN. "4365318".PN. "4523313".PN. "4587481".PN. "4698807".PN.).CCLS. and (self-test or self-check or selfcheck or selftest)	USPAT; US-PGPUB	2003/01/02 14:38
-	0	(712/2 ((714/10).CCLS. and ("IBM" "BUSINESS MACHINES").AS.) "3681578".PN. "4365318".PN. "4523313".PN. "4587481".PN. "4698807".PN.).CCLS. and (self-test or self-check or selfcheck or selftest)	USPAT; US-PGPUB	2003/01/02 14:38
-	1523	(712/2).CCLS. (714/3).CCLS. (714/4).CCLS. (712/6).CCLS. (714/7).CCLS. (714/8).CCLS. (714/9).CCLS. and (self-test or self-check or selfcheck or selftest)	USPAT; US-PGPUB	2003/01/02 14:39
-	83	((712/2).CCLS. or (714/3).CCLS. or (714/4).CCLS. (712/6).CCLS. or (714/7).CCLS. or (714/8).CCLS. (714/9).CCLS.) and (self-test or self-check or selfcheck or selftest)	USPAT; US-PGPUB	2003/01/02 14:40
-	83	((712/2).CCLS. (714/3).CCLS. (714/4).CCLS. (712/6).CCLS. (714/7).CCLS. (714/8).CCLS. (714/9).CCLS.) and (self-test or self-check or selfcheck or selftest)	USPAT; US-PGPUB	2003/01/02 15:47
-	12	vector adj1 processor adj1 element	USPAT; US-PGPUB	2003/01/02 18:02
-	152	(714/733).cor.	USPAT; US-PGPUB	2003/01/02 18:03
-	46	(714/733).cor. AND PROCESSOR	USPAT; US-PGPUB	2003/01/02 18:22
-	22	(714/734).cor. AND PROCESSOR	USPAT; US-PGPUB	2003/01/02 18:23
-	39	(714/732).cor. AND PROCESSOR	USPAT; US-PGPUB	2003/01/03 10:24
-	81	context adj switch and shadow adj register	USPAT; US-PGPUB	2003/01/03 10:24
-	3	context adj switch with shadow adj register	USPAT; US-PGPUB	2003/01/03 12:41
-	1	"5815701".PN.	USPAT	2003/01/03 10:25
-	1	"5717933".PN.	USPAT	2003/01/03 10:27
-	1	"5644772".PN.	USPAT	2003/01/03 10:28

-	1	"5530873".PN.	USPAT	2003/01/03 10:29
-	1	("5657330").PN.	USPAT; US-PGPUB	2003/01/03 12:56
-	1	"4433413".PN.	USPAT	2003/01/03 12:41
-	1	"4433413".PN.	USPAT	2003/01/03 12:51
-	1	"5119378".PN.	USPAT	2003/01/03 12:51
-	1	"5157781".PN.	USPAT	2003/01/03 12:52
-	0	("processor same (selftest or self-test) with (subroutine or sub-routine)").PN.	USPAT; US-PGPUB	2003/01/03 12:57
-	0	("(selftest or self-test) with (subroutine or sub-routine)").PN.	USPAT; US-PGPUB	2003/01/03 12:58
-	24	processor.AB. and (selftest or self-test) with (subroutine or sub-routine)	USPAT; US-PGPUB	2003/01/03 14:18
-	1	("5657330").PN.	USPAT; US-PGPUB	2003/01/03 14:19
-	5	"5657330" and idle	USPAT; US-PGPUB	2003/01/03 15:13
-	0	memory adj "not" adj reset	USPAT; US-PGPUB	2003/01/03 15:14
-	0	save adj memory adj reset	USPAT; US-PGPUB	2003/01/03 15:14
-	1455	memory adj reset	USPAT; US-PGPUB	2003/01/03 15:16
-	0	memory adj reset adj nonvolatile	USPAT; US-PGPUB	2003/01/03 16:35
-	0	PCT/DE98/03682	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/03 16:35
-	0	03682.APN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/03 16:35
-	33	"03682"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/03 16:36
-	0	"PROCESS FOR REPAIRING INTEGRATED CIRCUITS"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/03 16:37
-	0	PROCESS adj1 FOR adj1 REPAIRING adj1 INTEGRATED adj1 CIRCUITS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/03 16:37